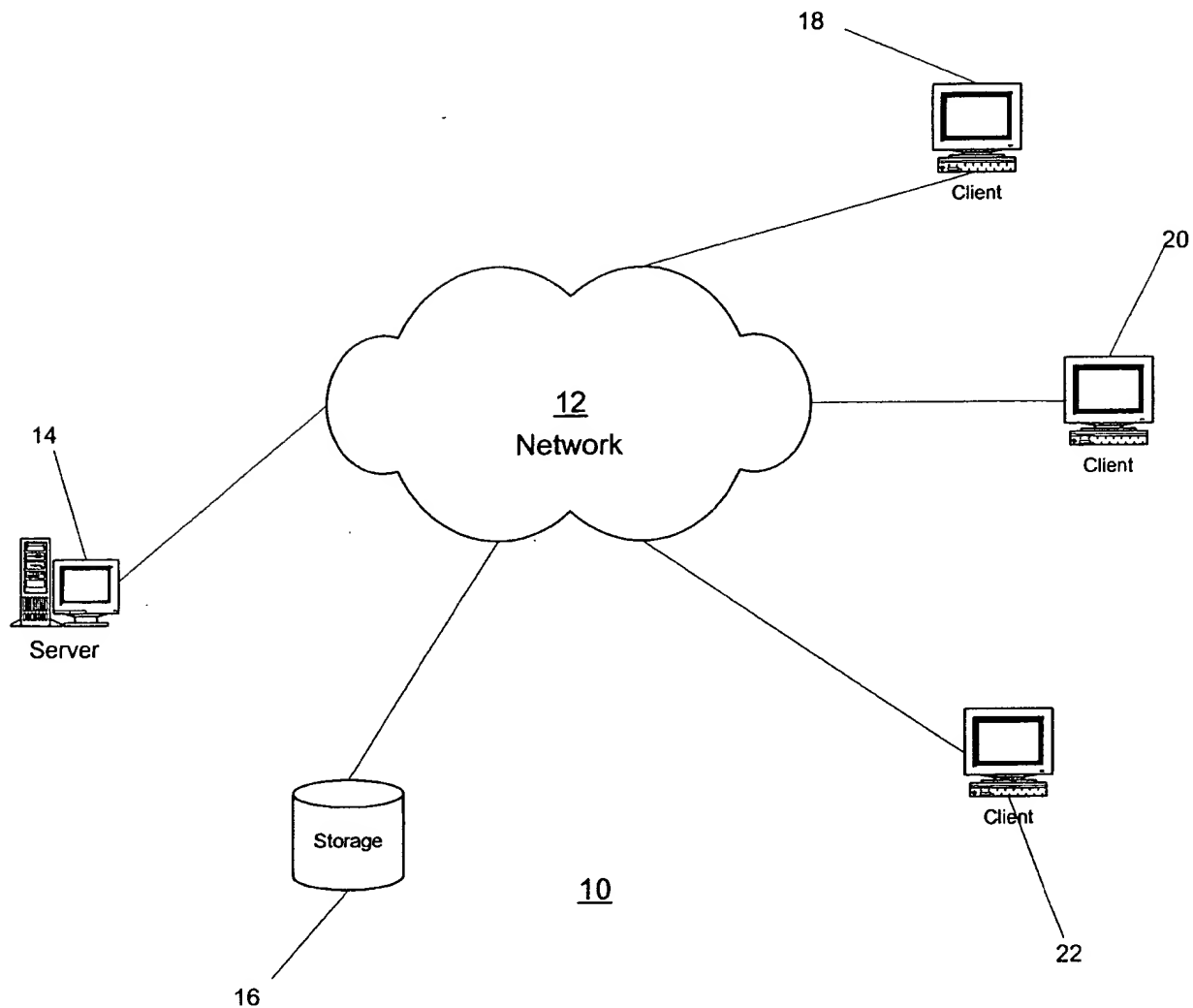
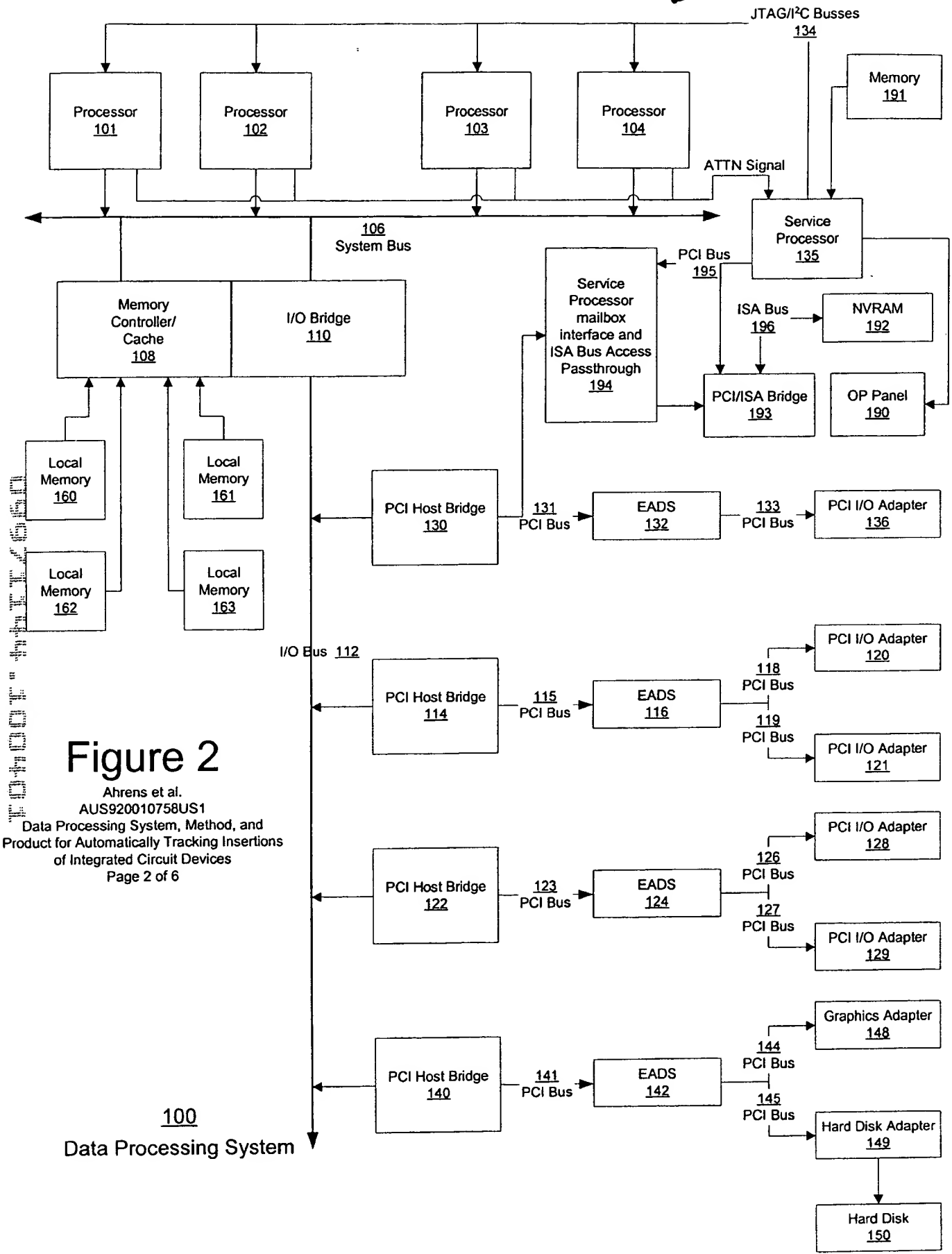


# Figure 1

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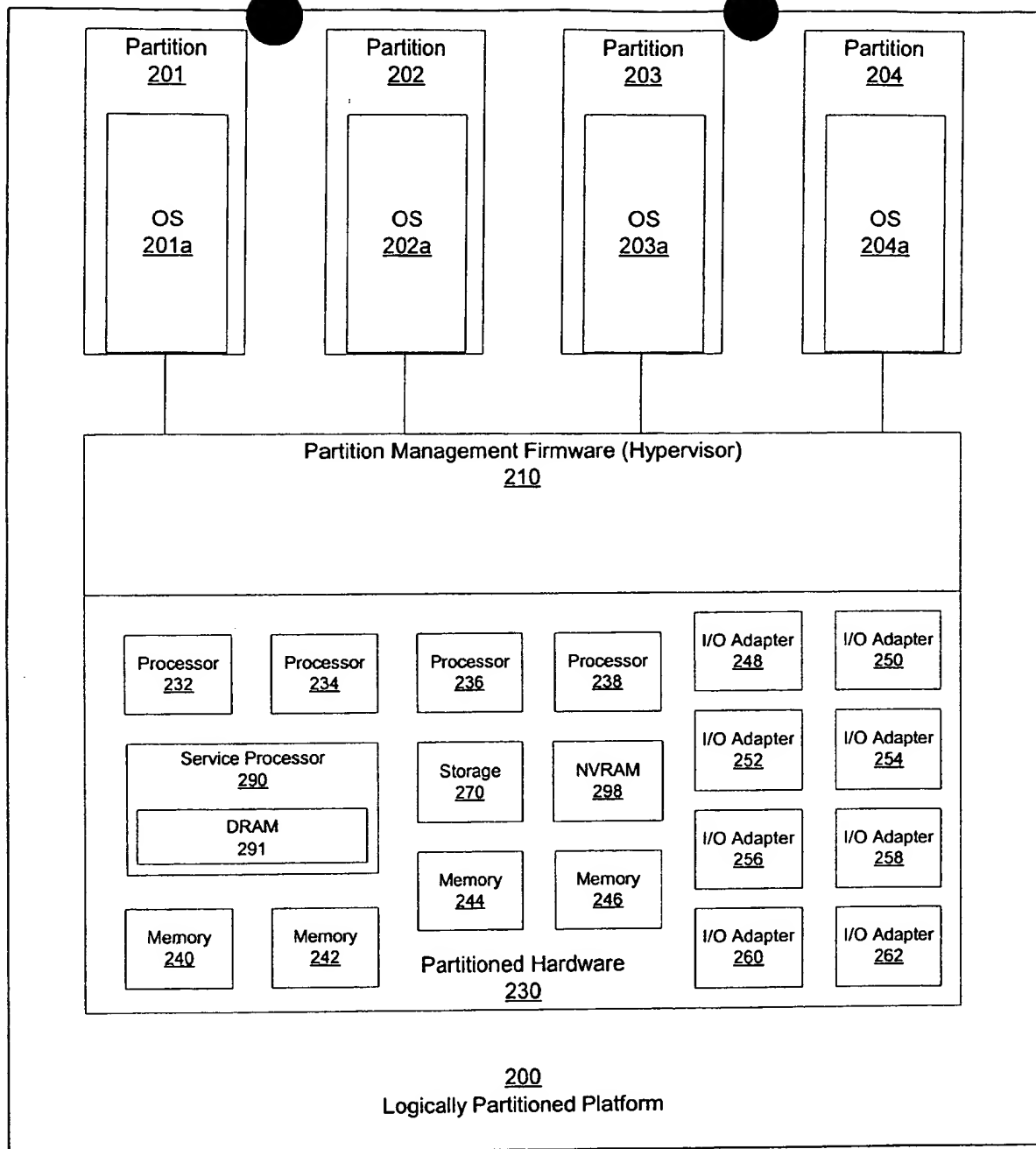


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**Figure 2**

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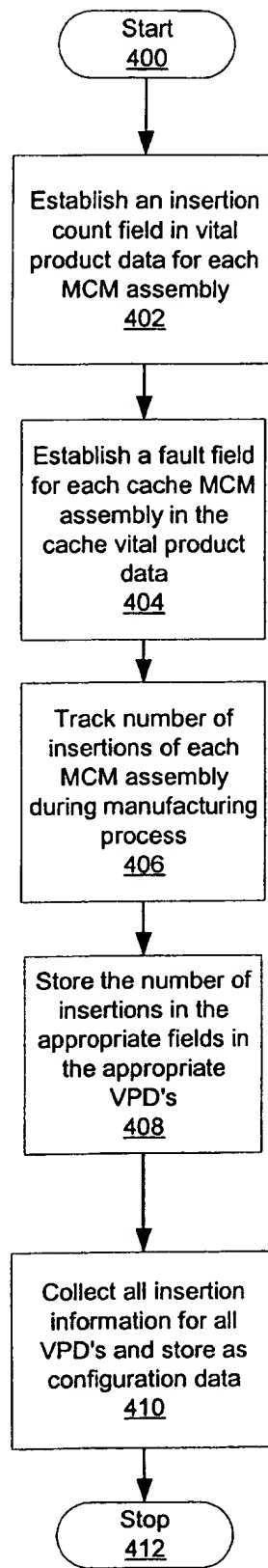
**Figure 3**

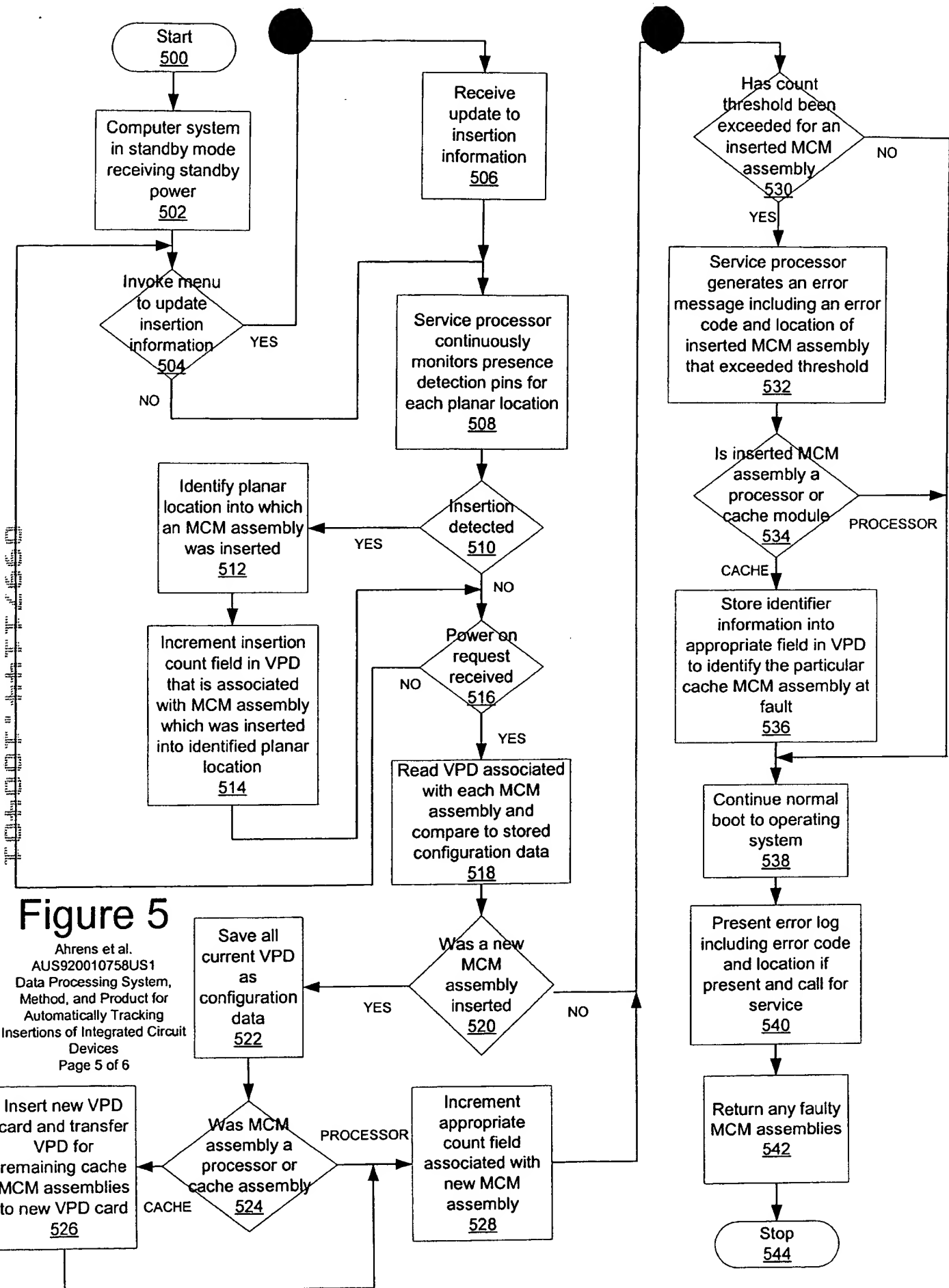
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## Figure 4

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**Figure 5**

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MCM Assembly Name	Location Code	Number of Times Inserted		Other fields
Processor 1-8	U1.3-P1-C1	2		
Processor 9-16	U1.3-P1-C2	7		
MCM Assembly Name	Location Code	Number of Times Inserted	Fault	Other fields
L3 Module 1	U1.3-P1-C5	3	0	
L3 Module 15	U1.3-P1-C19	10	1	
L3 Module 16	U1.3-P1-C19	1	0	

## Figure 6

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FOOTNOTES